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D

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Diagram illustrating the instruction format (32 bits total):

- Register 1 (R1):** 8 bits (bits 31-24), labeled "R1".
- Displacement 1 (D1):** 8 bits (bits 23-16), labeled "D1".
- Register 2 (R2):** 8 bits (bits 15-8), labeled "R2".
- Displacement 2 (D2):** 8 bits (bits 7-0), labeled "D2".
- Condition Code (C):** 1 bit (bit 0), labeled "C".

The diagram shows the bit positions for each field and the corresponding register numbers (R1, R2) and displacement values (D1, D2).

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0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

Sl 2 → 4 0 4 0 2 0 2 0 2 0 2 0 2 0 0 0

0 2 3 0 2 3 0 0 0 0 0 0 0 0 0 0